



Gumroad Yu Cheng Hong Collection. 3Dconcept artist Yu Cheng Hong posted a high definition screen saver with character art to his Gumroad account.

1. Field of the Invention This invention relates to an integrated circuit which is constructed by forming a plurality of semiconductor devices or elements on one and the same substrate. More particularly, this invention relates to an improvement of the structure of the integrated circuit in which a conductive layer having a stepped surface is formed over one surface of the substrate and a device isolation insulating layer is formed between the conductive layer and the substrate.

2. Description of the Related Art In general, there is adopted a so-called well-known LOCOS (Local Oxidation of Silicon) method as a method of forming a device isolation insulating layer on a silicon substrate. This LOCOS method is carried out by a process which comprises the steps of: (1) forming a silicon nitride film or the like on the surface of the silicon substrate as a thermal oxidation preventing mask layer and (2) carrying out a selective thermal oxidation process by using the mask layer as a mask to thereby form a field oxide layer as an oxide mask. In order to further increase the density of integration of the semiconductor devices formed on a semiconductor substrate, it is proposed to employ, in addition to the above-mentioned LOCOS method, a self-aligned isolation method which enables the formation of a device isolation

insulating layer with a width much smaller than that of the field oxide layer formed by the LOCOS method. The self-aligned isolation method can be achieved by means of, for example, a so-called trench isolation method which is carried out by a process comprising the steps of: (1) forming a trench which is to be used as a device isolation insulating layer on the surface of a silicon substrate; (2) forming a silicon nitride film or the like on the surface of the substrate so as to cover the trench; (3) filling the trench with an oxide film to form an insulating layer; and (4) removing the silicon nitride film or the like to expose the oxide film and thus form a device isolation insulating layer in the trench. However, when the well-known self-aligned isolation method is adopted, the field oxide layer which is to be formed by the LOCOS method is not formed on the entire surface of the substrate, but only in a region where the substrate surface portion lies within the region where the field oxide layer is to be formed

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