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The Intel FPGA 25G Ethernet IP Core is a simulation testbed and hardware design example that supports hardware compilation and testing. The 25G Ethernet FPGA IP Core provides the hardware required for the 25G Ethernet FPGA IP Core and has a total of 25 Ethernet ports (each port can connect to one or more FPGA 25G Ethernet IP Cores) and 25 GPIO ports. It features two Intel Core i5 processors and one Intel Xeon Platinum 8180 processor. It supports up to four IP cores, two processors, 32GB of RAM and 32GB of ECC memory. It supports up to 256GB of DDR4-2400 memory.

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